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The Dini Group Introduces the DN6000k10PCIe for the new PCI Express Bus. Here are 4.5 million ASIC gates on an easy to use, PCIe hosted board. The DN6000k10PCIe supports up to 6, 2vp100 VirtexII-Pro FPGA's, with an incredible amount of FPGA to FPGA interconnect for easy logic partitioning. FPGA's are interconnected with rocket I/O's, enabling the movement of data between them at 100's of GB/s. One, four and eight lane versions are available with ten DDR memories (64Mx16) and four SSRAMS (2Mx36) external to the FPGAs.

Now ASIC and FPGA designers using the PCI Express Bus can prototype logic designs for a fraction of the cost of existing solutions. "Here is the best solution for PCIe ASIC prototyping," says Mike Dini president of The Dini Group.

"Developers of complex, high speed ASICs will find this board to be the most cost effective way to emulate their designs at anything like target frequencies."

The DN6000k10PCIe will be supplied with 2-6 FPGAs; prices start at only \$12,750. They are available "off-the-shelf" directly from The Dini Group and their agents.

The Dini Group was established in 1995 as a consulting company. While developing ASICs for various clients they saw the need for cost effective logic emulation platforms and developed several of them. In 1998 they started selling these platforms to ASIC developers and FPGA system users. From their offices in La Jolla the dozen Dini Group employees have supplied over one billion ASIC gates.