

December 19, 2005

News from The Dini Group
For Immediate Release



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24 Million ASIC gates on new high-speed Virtex-4 based FPGA Prototyping Engine

The DINI Group Introduces the DN8000K10 ASIC Prototyping Engine featuring new larger and faster FPGA's. This high-speed board supports up to 14 LX family and two FX family Xilinx Virtex-4 FPGA's, with an incredible amount of FPGA to FPGA interconnect for easy logic partitioning. The FPGA's are interconnected by source synchronous LVDS and all chip to chip interconnect runs at 350MHz (700 mbits/sec). Using double-data rate (DDR) mode and embedded 10X pin multiplexing, the DN8000k10 represents a quantum leap in the quantity of FPGA to FPGA interconnects and greatly simplifies the nastiest problem of logic prototyping. Fully configured the DN8000k10 has 2.6 million flip-flops/LUT pairs, representing nearly 37 million total available gates (assuming 14 gates for a FF/LUT pair). At a utilization of 60%, the DN8000k10 comfortably supports 24 million gates of logic, measured reasonably and conservatively.

The new board will allow ASIC developers to run high-speed emulations of huge designs at much higher clock frequencies. "This board will meet the needs of our customers who are building ever larger chips," says Mike Dini president of The DINI Group; "for big, complex designs, prototyping is mandatory. The DN8000k10 will aid verification and run emulation at very high-speed." Custom daughter cards (up to 8) adapt the board for almost any application.

User interface includes USB, multiple RS232 ports and Multi-Gigabit Transceivers in the Rocket I/O subsystem. Four DDR2 SODIMM modules provide up to 8 gigabytes of external memory. Alternate, pin-compatible SODIMM modules are available containing FLASH, SSRAM, QDR SSRAM, Mictor(s), and RLDRAM. Several gigabit serial interfaces are included via XFP and SFP modules, along with SMA and Samtec QSE connectors. There are 7 clock networks, clocks can be sourced from the daughter cards and driven into the main clock networks of the DN8000k10.

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Hardware for ASIC Prototyping & FPGA Systems



The board is supplied standard with a metal carrier and power supply. An optional 4U chassis is available that enables rack-mounting and additional cooling. Software support for embedded logic analyzers (ChipScope, Identify™) and partitioning (Synplicity's, Certify™) is included.

The DN8000k10 may be ordered with 2 to 16 FPGAs in various sizes and speed ranges. Prices start at only \$22,000 and all versions break the 1-cent/gate threshold. The DN8000k10 is available "off-the-shelf" directly from The DINI Group and its agents with lead-times in all configurations less than 3 weeks.

The DINI Group was established in 1995 as a consulting company. While developing ASICs for various clients they saw the need for cost effective logic emulation platforms and developed several of them. In 1998 they started selling these platforms to ASIC developers and FPGA system users. From their offices in La Jolla California, The DINI Group is one of the world's leading suppliers of FPGA-based logic prototyping hardware and has supplied this expanding market with over two billion ASIC gates.

-30-