



June 1, 2007

For Immediate Release

Agency Contact: Rob Britton, Electronics Marketing Group
619-231-6907

Company Contact: Mike Dini, President
858-454-3419

New 4-lane PCIe Board with Virtex-5 FPGAs for High-Speed Prototyping

The DINI Group Introduces the DN9000K10PCIe-4GL featuring Xilinx, Virtex-5 FPGAs. This high-density board is a complete logic emulation system that provides ASIC or IP designers an easy to use platform for high-speed prototyping.

The board is hosted on a 4-lane PCIe bus or used stand-alone, with a separate power supply. The 6 FPGAs will emulate more than 12 million ASIC gates, 100% of FPGA resources are available to the user application.

The board allows ASIC and FPGA designers to prototype logic designs and run them at near real time clock speeds in multiple FPGAs. "This board runs with no speed limit," says Mike DINI president of The DINI Group; "and the 400MHz LVDS chip to chip interconnects make for easy partitioning."

The DN9000K10PCIe-4GL will be supplied with 2 to 6 FPGAs in various sizes and speed ranges; prices start at only \$12k. They are available "off-the-shelf" directly from The DINI Group and its agents.

The DINI Group was established in 1995 as a consulting company. While developing ASICs for various clients they saw the need for cost effective logic emulation platforms and developed several of them. In 1998 they started selling these platforms to ASIC prototypers, FPGA developers, and IP designers. From their offices in La Jolla the dozen DINI Group employees have supplied over three billion ASIC gates.