

## DN3000K10 Stuffing Option Comparison

Stuffed FPGAs	PCI	Memory Access		TOTAL chip to chip connections	non-memory chip to chip connections	non-header, non-memory chip to chip connections	TOTAL header connections	single-FPGA header connections	single-FPGA memory	
		SDRAM	SSRAMs						SSRAMs	SDRAM
ABDEF	yes	yes	4	1401	1011	875	418	190	0	no
ABEF	yes	yes	4	999	749	617	360	171	2	no
ABDF	yes	yes	4	943	553	473	410	238	0	no
BDEF	yes	yes	4	940	690	614	343	266	2	no
ADEF	yes	yes	4	899	759	623	369	198	2	yes
ABF	yes	yes	4	609	429	353	352	219	3	no
BDF	yes	yes	4	597	347	271	279	202	2	no
ADF	yes	yes	4	530	390	310	361	246	2	yes
AEF	yes	yes	3	673	603	471	311	179	2	yes
BEF	yes	yes	3	612	432	356	281	204	2	no
DEF	yes	yes	3	565	495	419	294	218	2	yes
AF	yes	yes	3	372	372	296	303	227	3	yes
BF	yes	yes	3	337	227	151	182	105	3	no
DF	yes	yes	3	311	241	165	174	98	2	yes
EF	yes	yes	1	413	343	267	141	65	0	yes
F	yes	yes	1	0	0	0	77	77	1	yes
ABDE	no	yes	4	1044	764	628	418	191	0	yes
BDE	no	yes	4	728	588	512	343	267	2	yes
ABD	no	yes	4	674	394	314	410	239	0	yes
ABE	no	yes	4	628	488	356	360	172	2	yes
ADE	no	no	4	652	512	376	368	197	2	-
BD	no	yes	4	403	333	257	335	259	3	yes
AB	no	yes	3	354	284	208	352	220	2	yes
BE	no	yes	3	316	316	240	250	174	3	yes
AD	no	no	4	301	231	151	360	245	3	-
DE	no	no	3	463	393	317	237	161	2	-
AE	no	no	3	342	342	210	310	178	3	-
B	no	yes	2	0	0	0	182	182	2	yes
D	no	no	3	0	0	0	173	173	3	-
A	no	no	2	0	0	0	302	302	2	-
E	no	no	1	0	0	0	140	140	1	-

"chip to chip connections" means any net which connects two or more FPGAs together, regardless of what else it may connect

note that this includes FED and MB buses which connect more than two FPGAs

"non-memory chip to chip" means chip to chip connections that do not connect to any SSRAM or the SDRAM socket

"non-header, non-memory" means chip to chip connections that do not connect to memories or any of the 200-pin headers

"header connections" means any net which connects one or more FPGAs to a header

"single-FPGA header connections" means a header connection that only connects to one FPGA

"single-FPGA memory" means a memory device which is only connected to one FPGA