

!! Important Product Information: Do Not Discard !!



Product Notice XC2V8000 CES Devices

Dear Xilinx Customer,

Thank you for your interest in the enclosed Virtex-II devices. We would like you to be aware of the following information that has been updated in the Virtex-II datasheets and the Virtex-II Platform FPGA handbook. This information applies to all XC2V8000 devices with JTAG IDCODE revision code = **0000** (binary). If you have any questions, please contact your Xilinx FAE for assistance.

- 1. Bitstream Encryption** – Triple DES Bitstream Encryption function is available in this device; however, the device needs a special test screen to ensure complete functionality. If an incorrectly keyed bitstream is loaded into the FPGA (i. e., wrong keys or no keys are programmed into the device), the device could draw an excessive amount of current. This could result in the device getting hot and becoming permanently damaged. Please contact your Xilinx FAE or local Sales Office for further details and ordering information.