

DN5000K10 Stuffing Option Comparison

Stuffed FPGAs	PCI	Memory Access		Total chip to chip connections	non-memory chip to chip connections	non-header, non- memory chip to chip connections	TOTAL header connections	single-FPGA header connections	single- FPGA SSRAMs
		SDRAM	SSRAMs						
ABDEF	yes	yes	4	1874	1601	1377	488	0	0
ABDE	no	yes	4	1307	1103	984	488	171	1
ABDF	yes	yes	4	1274	1067	780	488	0	1
ABEF	yes	no	4	1310	1175	966	488	150	2
ADEF	yes	yes	4	1122	984	945	488	314	2
BDEF	yes	yes	4	1121	983	965	488	335	2
ABD	no	yes	4	865	727	542	488	171	2
ABE	no	no	4	743	674	573	488	321	3
ABF	yes	no	3	865	727	521	488	150	1
ADE	no	yes	3	708	573	552	338	185	1
ADF	yes	yes	4	665	596	491	488	314	3
AEF	yes	no	4	814	814	790	488	464	4
BDE	no	yes	4	810	744	807	467	464	3
BDF	yes	yes	4	665	596	509	488	335	3
BEF	yes	no	3	710	641	554	338	185	2
DEF	yes	yes	3	569	503	566	324	321	2
AB	no	no	3	456	387	286	488	321	2
AD	no	yes	3	409	340	253	338	185	2
AE	no	no	4	400	400	397	338	335	4
AF	yes	no	3	512	512	488	488	464	3
BD	no	yes	3	512	512	509	467	464	3
BE	no	no	3	399	399	396	317	314	3
BF	yes	no	2	409	409	253	338	185	1
DE	no	yes	2	411	345	408	153	150	1
DF	yes	yes	3	256	256	253	324	321	2
EF	yes	no	2	414	414	411	174	171	2
A	no	no	2	0	0	0	338	338	2
B	no	no	2	0	0	0	317	317	2
D	no	yes	2	0	0	0	153	153	2
E	no	no	1	0	0	0	3	3	1
F	yes	no	1	0	0	0	174	174	1