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New Virtex-5 based FPGA Prototyping Engine offers 12 Million ASIC gates and 30% faster clock speeds.

The DINI Group Introduces the DN9000K10PCI featuring Xilinx, Virtex-5 FPGAs. This high-density board supports 6 LX family Virtex-5 FPGAs; with 65nm process, 6 input LUT, and advanced interconnect. All of the resources of these FPGAs are available for programming by the user.

The board is hosted in a 32-bit or 64-bit PCI slot (33/66 MHz) or used stand-alone, with a separate power supply. An independent PCI controller, 7 Global Clock networks, and massive on-board memory provided by 6 DDR2 SODIMM sockets will provide unmatched speed and performance.

This board allows ASIC and FPGA designers to prototype logic designs and run them at near real time clock speeds in multiple FPGAs. “Our customers are always looking for the biggest and the fastest,” says Mike DINI president of The DINI Group; “we ran our benchmarks on the Virtex-5 and they all perform 30% faster. ”

The DN9000K10PCI will be supplied with 2 to 6 FPGAs in various sizes and speed ranges with prices start at only $12k. They are available “off-the-shelf” directly from The DINI Group and its agents.

The DINI Group was established in 1995 as a consulting company. While developing ASICs for various clients they saw the need for cost effective logic emulation platforms and developed several of them. In 1998 they started selling these platforms to ASIC prototypers, FPGA developers, and IP designers. From their offices in La Jolla the dozen DINI Group employees have supplied over three billion ASIC gates.

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