

# DNSODM200\_ QDR

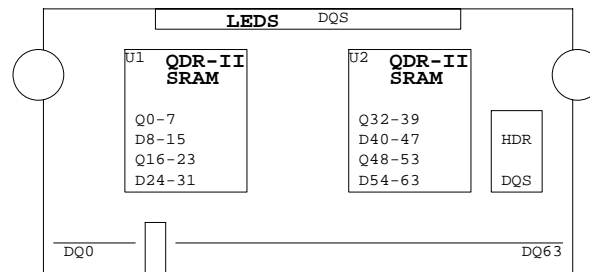
DDR2 SODIMM form factor

2 QDR-II SRAMs (16bit) 36Mbit

300Mhz Max. clock

80Mhz min clock

8 LEDS



Drawn

**The Dini Group**  
La Jolla, inc.

Project sram\_mod (SRAM)

Approved

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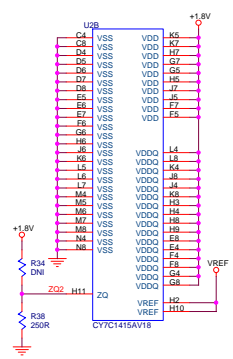
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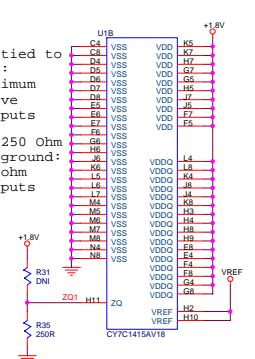
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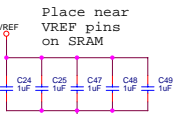
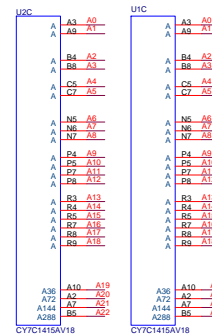
Tuesday, March 07, 2006



ZQ tied to VDD: maximum drive outputs  
ZQ 250 Ohm to ground: 50 ohm outputs



CTS 746x101 25 mil pitch arrays



### Bypass Network

