

Errata

- Problem:** The Secondary PCI signal DEVSEL- is not on the PCI signal header J5.

Solution: DEVSEL- is now on pin 18 on header J7. This signal does go through the CPLD so there is a 7.5ns delay between DEVSEL- and the pin on the header.
- Problem:** The Secondary PCI signal REQ64- is not on the PCI signal header J5.

Solution: REQ64- is now on pin 2 on header J7. This signal does go through the CPLD so there is a 7.5ns delay between REQ64- and the pin on the header.
- Problem:** The Secondary PCI signal AD28 is not on the PCI signal header J5.

Solution: Connect a wire from pin A22 on the PCI connector labeled J3 to pin 15, 17 or 19 of header J6.

Instructions for finding pin A22 on J3:

First locate the group of solder blobs that are on the bottom of the board which are directly underneath the PCI connector labeled J3 on the top of the board. Then turn the board upside down with the straddle mount PCI connector (labeled SLOT 2) on the left, pin A22 is in the second column from the left 11 pins down from the top.

