Features

- Camera Link Interface
  - 5 channels input
  - 6 channels output
  - 26-pin MDR connectors

- Xilinx VirtexII-Pro FPGA
  - 2vp20, 2vp30, 2vp40, or 2vp50
  - –5, -6, or –7
  - 1152 BGA package

- 200-pin connector for high-speed mating to:
  - DN3000k10/S
  - DN6000k10/SE/PCI/PCIe
  - DN7000k10
  - DN8000k10

- Can operate stand-alone
- 8 Channels RocketI/O (TX/RX)
  - SMB connectors (4 per channel)
  - High-speed serial to 3.125 GB/s per channel
    - With –6 or –7 speed grade FPGA
- RS232 ports for PowerPC processor visibility
  - 2 Tx/Rx, 1 Tx only
- Video Input Processor (9-bit)
  - Philips SAA7113H
  - I2C Connection to FPGA
  - PowerPC Setup/Configuration
    - Two PowerPC 405 Cores (in FPGA)
    - Embedded 300+ MHz Harvard Architecture
    - Hardware Multiply/Divide Unit
    - Thirty-Two 32-bit Gen. Purpose Registers

- 16 KB 2-Way Set-Assoc. Instruction Cache
- 16 KB 2-Way Set-Associative Data Cache
- Memory Management Unit (MMU)
- Timer Facilities
- Full support for embedded logic analyzers (via JTAG)
  - ChipScope, ChipScope PRO
  - Identify™ from Synplicity
- Self contained power
  - Only an external +5V is required
- 2 Programmable Clock Generators
  - ICS8442
- 24 pairs (or 48 single-ended) High Speed I/O
  - Selectable Vref
  - Selectable VCCio
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